EP 0 997 836 A2

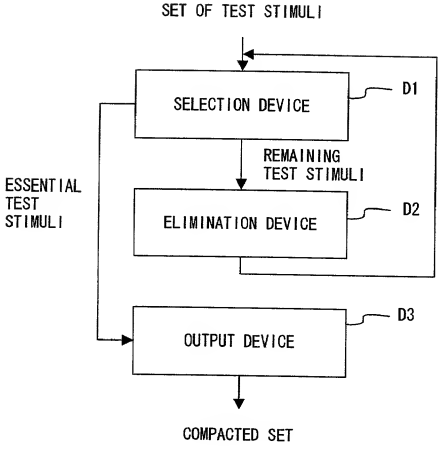

FIG. 1

Test Vector \ Fault	f_1	f_2	f_3	f_4	f_5	f_6	f_7	f_8	f_9	f_{10}	f_{11}	f_{12}
t_1	×	×		×			×					
t_2	×		×	×				×				
t_3		×			×		×			×		
t_4			×			×						
t_5					×				×			
t_6				×			×	×			×	
t_7						×	×					×

FIG. 2

EP 0 957 367 A1

Test Vector \ Fault	f_1	f_3
t_1	×	
t_2	×	×
t_4		×

FIG. 3

US 6,155,927 - 12/05/2000

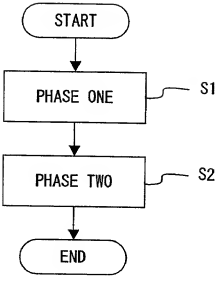

FIG. 4

JP2001-297258A

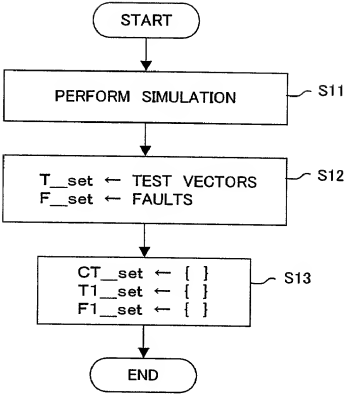

FIG. 5

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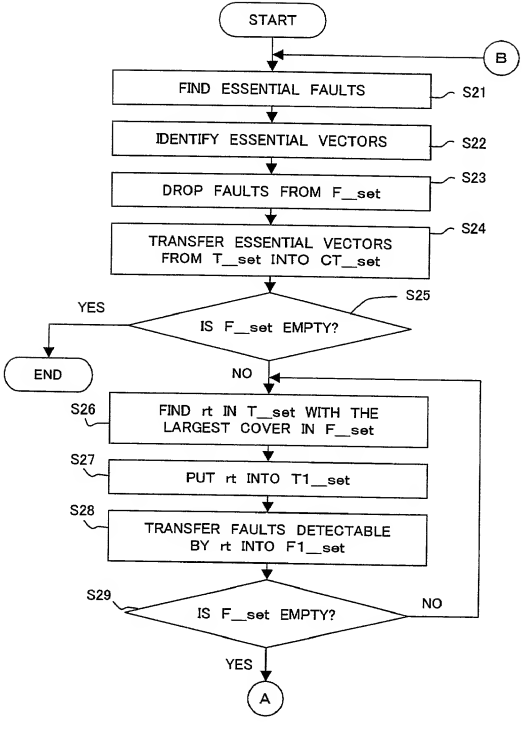


FIG. 6

JP2004150960A

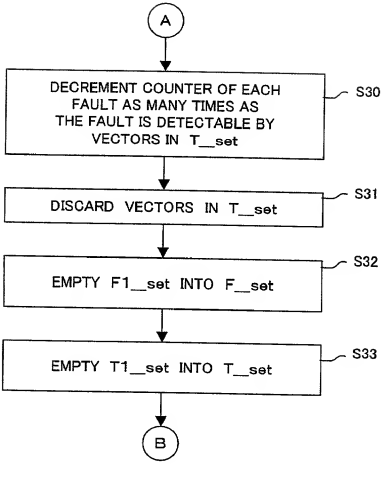


FIG. 7

EP 0 855 659 A2

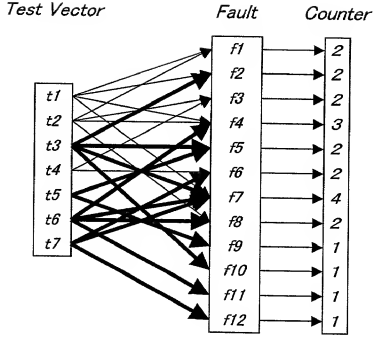


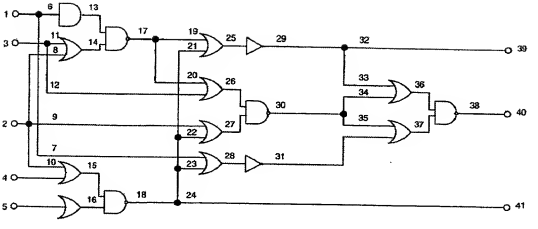
FIG. 8

0000578 "11/06/01

FIG. 9

0903576B1

FIG. 10


1
3
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24
41

FIG. 11

EP 0 924 526 A2

Test Vector	Pattern
t_1	0 1 2 2 1
t_2	1 1 2 2 1
t_3	2 0 2 0 1
t_4	2 1 2 0 1
t_5	1 0 0 1 1
t_6	1 0 1 1 1
t_7	2 0 2 1 1
t_8	2 1 2 2 0
t_9	2 1 2 2 1
t_{10}	1 0 0 1 0
t_{11}	1 1 0 1 1
t_{12}	0 0 1 1 1
t_{13}	0 1 1 1 1
t_{14}	1 1 0 2 1
t_{15}	1 1 1 2 1
t_{16}	0 1 0 2 0

FIG. 12

EP 0 997 836 A2

Fault	Counter	Test Vector	#Fault
1/1	3	t_1	6
5/1	3	t_2	0
6/1	3	t_7	0
8/0	2	t_8	2
12/1	2	t_9	0
13/1	3	t_{10}	2
16/1	3	t_{11}	4
20/1	2	t_{12}	3
26/1	2	t_{13}	6
33/1	2	t_{14}	4
34/1	2	t_{16}	2
36/1	2		

FIG. 13

JP2001-357660

FIG. 14

0988571 .1 0988571

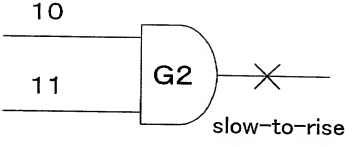

FIG. 15

1998-1-2 9:56:60

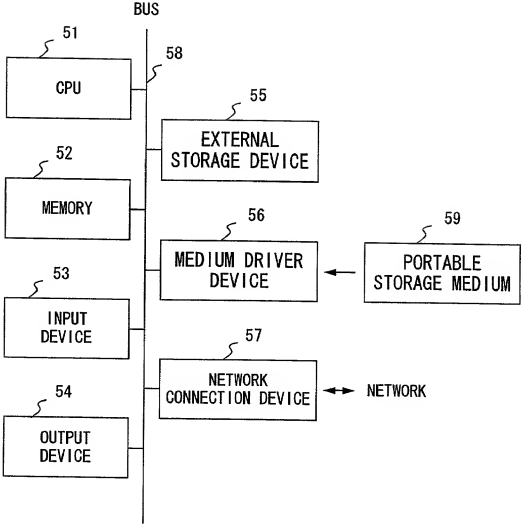

FIG. 16

EP 1 991 957 A1

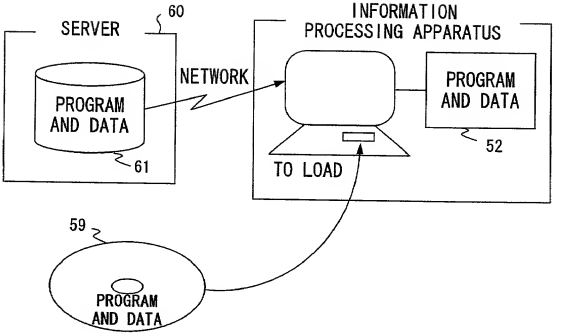

FIG. 17